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KL8-J
Engineering Drawings
Digital Equipment Corporation

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| CUSTOMER PRINT SET | | DRAWING NO. | | NO OF REV | DESCRIPTION | OPTION NO./FILE DATE |
|--------------------|-----|----------------|--|-----------|---|----------------------|
| 1 | 234 | | | * | TERM CONT/ASYNC DATA INT ENGINEERING SPEC IN HOUSE AND FIELD SERV ACCEPT PRODUCTION TEST PROCEDURE ACCESSORY LIST | |
| X | | A-PL-KL8-J-0 | | 1 | | |
| X | | A-SP-KL8-JA-1 | | | | |
| X | | A-SP-KL8-JA-2 | | | | |
| X | | A-SP-KL8-JA-3 | | | | |
| X | | A-AL-KL8-JA-4 | | | | |
| X | | D-CS-M8655-0-1 | | 4 | TERMINAL CONTROL | |
| | | K-CO-M8655-0-4 | | 1 | X-Y CO-ORDINATE HOLE LOC | |
| | | D-AH-M8655-0-5 | | 1 | ASSY/DRILLING HOLE LAYOUT | |
| | | B-MH-M8655-0-6 | | 1 | MODULE ECO HISTORY | |

CUSTOMER PRINT SET

TITLE
TERM CONT/ASYNC DATA INT

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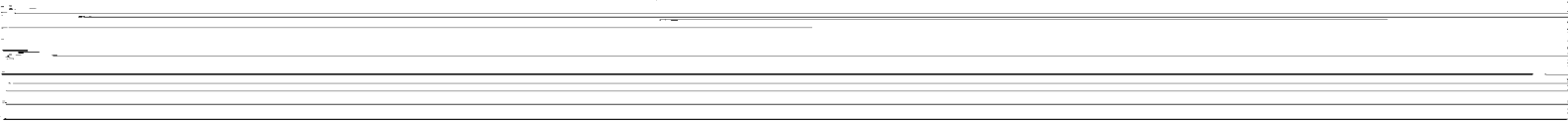
DESCRIPTION

SIZE CODE
B DD

SHEET 2 OF 2

NUMBER
KL8-J

REV



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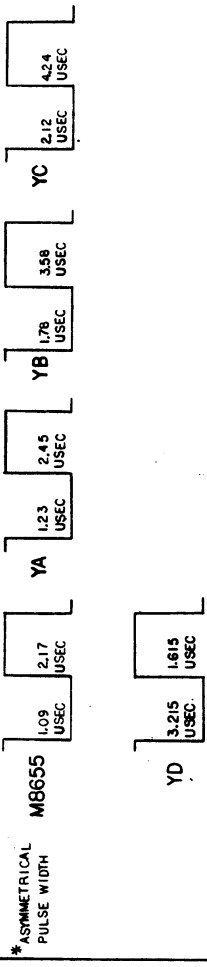
M8655 VARIATION TRANSLATION TABLE

| BL B2 B3 | M8655 OSCL 5.0688MHz DEC *18-11660-02 CLK PULSE WD / RATE | M8655-YA OSCL 4.438MHz DEC *18-11660-11 CLK PULSE WD / RATE | M8655-YB OSCL 3.0737MHz DEC *18-11660-08 CLK PULSE WD / RATE | M8655-YC OSCL 2.619MHz DEC *18-11660-08 CLK PULSE WD / RATE |
|-------------|--|--|---|--|
| OFF OFF OFF | 284 USEC/10 208 USEC/150 104 USEC/300 | 326 USEC/NA 236 USEC/NA 171 USEC/NA | 400 USEC/NA 343 USEC/NA 171 USEC/NA | 488 USEC/NA 201 USEC/NA |
| OFF ON OFF | 521 USEC/600 | 55.6 USEC/NA | 86 USEC/NA | 100 USEC/NA |
| ON ON OFF | 261 USEC/1200 | 29.8 USEC/1050 | 43 USEC/NA | 50.5 USEC/NA |
| ON OFF ON | 131 USEC/2400 | 14.9 USEC/NA | 21.4 USEC/NA | 25.1 USEC/NA |
| ON ON ON | 6.5 USEC/4800 | 7.44 USEC/NA | 10.7 USEC/NA | 12.6 USEC/NA |
| ON ON ON | 3.25 USEC/9600 | 3.62 USEC/NA | 5.37 USEC/NA | 6.35 USEC/NA |
| ON ON ON | 1.63 USEC/19200 * | 1.87 USEC/NA * | 2.67 USEC/NA * | 3.17 USEC/NA * |

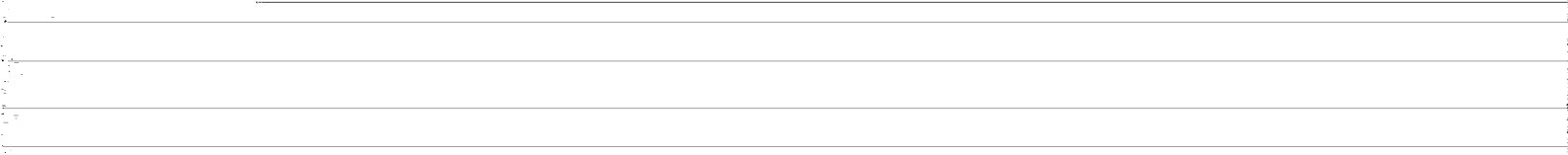
PULSE NAME TRANSLATION TABLE

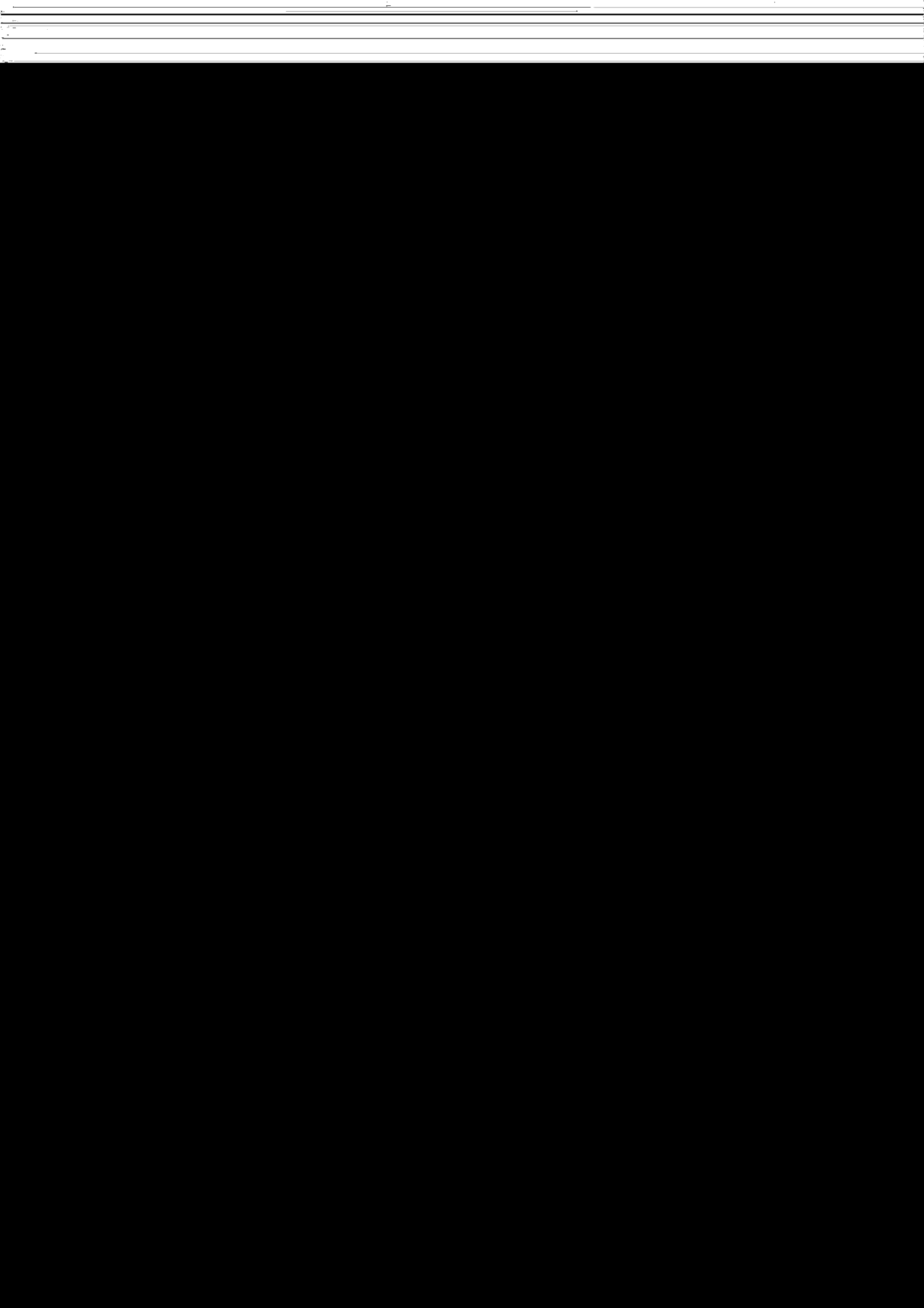
| SIGNAL NAME (ORIGINATING PIN) | PULSE WIDTH FOR M8655 | PULSE WIDTH FOR M8655 YA | PULSE WIDTH FOR M8655 YB | PULSE WIDTH FOR M8655 YC |
|--|---|---|---|---|
| P99 NSEC (E83-03) P2561 SEC (E88-05) P592 NSEC (E88-08) P3.28 MICROSEC (E15-09) P1.09 MICROSEC (E15-12) P17.8 MICROSEC (E14-11) | 99 NSEC 2561 NSEC 592 NSEC 3.28 MICROSEC 1.09 MICROSEC 17.8 MICROSEC | 14 NSEC 339 NSEC 677 NSEC 3.74 MICROSEC 11.23 MICROSEC 20.2 MICROSEC | 163 NSEC 489 N SEC 976 N SEC 5.39 MICROSEC 17.9 MICROSEC 29.3 MICROSEC | 192 NSEC 573 NSEC 114 MICROSEC 63 MICROSEC 2.12 MICROSEC 33.4 MICROSEC |
| SIGNAL NAME (ORIGINATING PIN) | PULSE WIDTH FOR M8655 - YD | | | |
| P99 NSEC (E83-03) P256 NSEC (E88-05) P592 NSEC (E88-08) P3.28 MICROSEC (E15-09) P1.09 MICROSEC (E15-12) P17.8 MICROSEC (E14-11) | 146 NSEC 256 NSEC 592 NSEC 3.28 MICROSEC 1.09 USEC 17.8 USEC | | | |

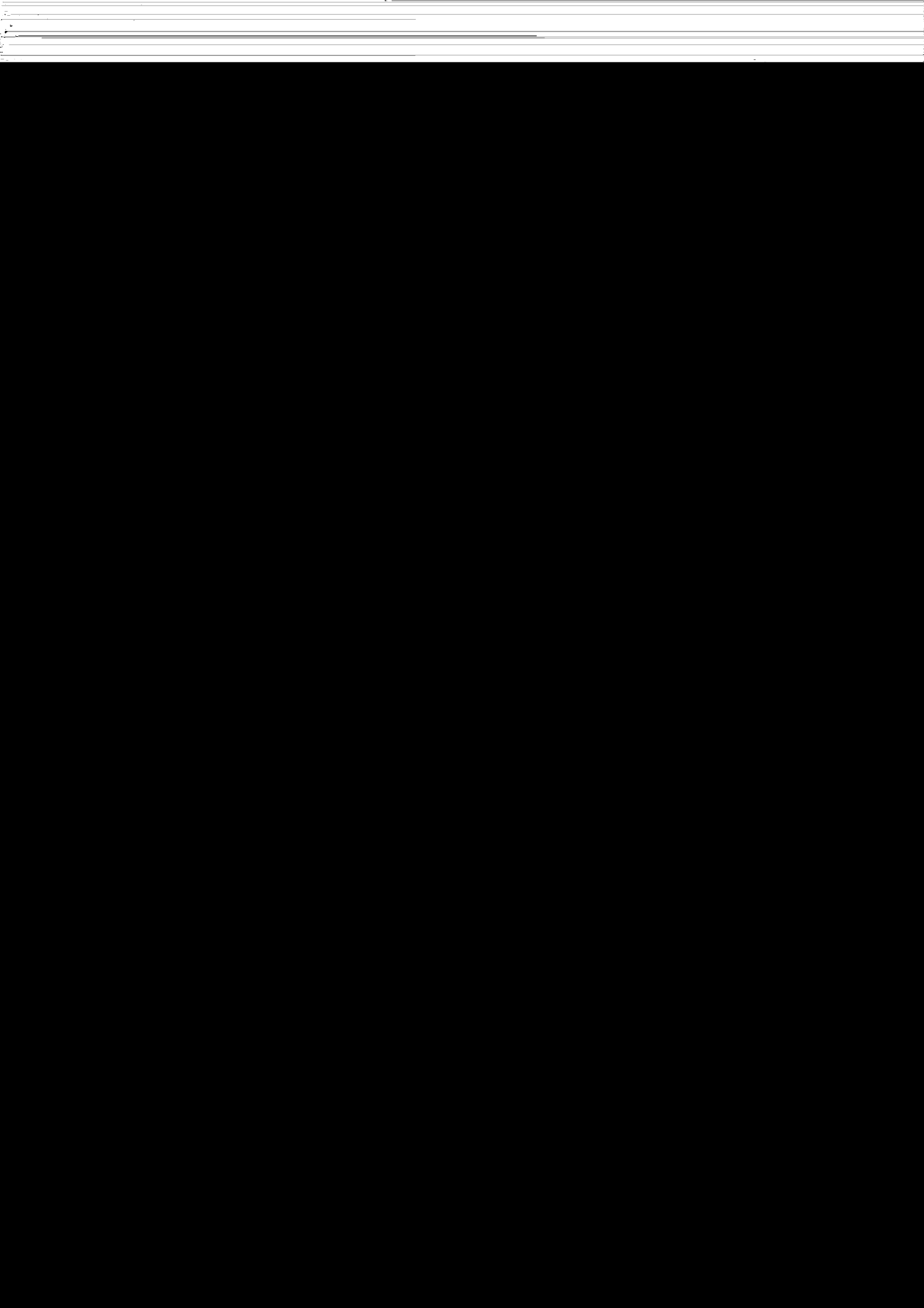
NOTE: DIFFERENTIATION BETWEEN M8655-YA, YB, YC AND YD MODULES IS THE XTAL SELECTION.
* W2 OUT, W5 IN FOR THESE CLOCK PULSES - W2 IN W5 OUT FOR ALL OTHER CASES.



NOTE: THIS TABLE IN REFERENCE TO SHEET 3 OF M8655 LOGIC DIAGRAM.







[Redacted]

[Redacted]

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